**Laboratorio 5: Projeto de circuitos lógicos usando mapa de Karnaugh**

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TABELA

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| M | F1 | F2 | F3 |  | ABRIR |
| 0 | 0 | 0 | 0 |  | 0 |
| 0 | 0 | 0 | 1 |  | 1 |
| 0 | 0 | 1 | 0 |  | 1 |
| 0 | 0 | 1 | 1 |  | X |
| 0 | 1 | 0 | 0 |  | 1 |
| 0 | 1 | 0 | 1 |  | X |
| 0 | 1 | 1 | 0 |  | X |
| 0 | 1 | 1 | 1 |  | X |
| 1 | 0 | 0 | 0 |  | 0 |
| 1 | 0 | 0 | 1 |  | 0 |
| 1 | 0 | 1 | 0 |  | 0 |
| 1 | 0 | 1 | 1 |  | 0 |
| 1 | 1 | 0 | 0 |  | 0 |
| 1 | 1 | 0 | 1 |  | 0 |
| 1 | 1 | 1 | 0 |  | 0 |
| 1 | 1 | 1 | 1 |  | 0 |

MAPA DE KARNAUGH

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  |  | F3 | F2F3 | F2 |
|  | 0 | 1 | X | 1 |
| F1 | 1 | X | X | X |
| MF1 | 0 | X | X | X |
| M | 0 | 0 | X | 0 |